

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

PAUL E LUSE, ET AL.

Application No.: 10/090,944

Filed: March 4, 2002

For: **CONTROL PROCEDURE SELECTION**

Art Group: 2186

Examiner: B. Peikari

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97**

Commissioner for Patents  
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The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

  
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Date: April 12, 2004

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Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				<i>Complete if Known</i>	
				Application Number	10/090,944
				Filing Date	3.4.02
				First Named Inventor	Paul Luse
				Art Unit	
				Examiner Name	
Sheet	1	of	2	Attorney Docket Number	42P18502

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
✓	1	Adaptec - Adaptec SCSI RAID 2000S Support		
✓	2	Adaptec - Adaptec SCSI RAID 2005S Support		
✓	3	Adaptec - Motherboard Partners and Compatibilty		
✓	4	Adaptec - Adaptec SCSI RAID 2005S Technical SPecs		
✓	5	Adpatec - Adaptec Article: Zero Channel RAID Frequently Asked Questions		
✓	6	Intel Corp., Product Brief, Intel Server Board SCB2		
✓	7	Serial ATA: High Speed Serialized AT Attachment Revision 1.0, 29 August 2001		
✓	8	Super0, Super P3TDDR, User's Manual, Revision 1.0a		
✓	9	PCI Local Bus, PCI BIOS Specification, Revision 2.1 August 26, 1994		
✓	10	PCI Local Bus, PCT-to-PCI Bridge Architecture Specification, Revision 1.1, December 16, 1998		
✓	11	PCI Local Bus, PCI Local Bus Specification, Revision 2.2, December 18, 1998		
✓	12	PCI Local Bus, Mini PCI Specification, Revision 1.0, October 25, 1999		
	13	PCI Local Bus, PCI-X Addendum to the PCI Local Bus Specification, Revision 1.0a, July 24, 2000		
	14	PCI Local Bus, PCI Local Bus Specification, Revision 2.3, March 29, 2002		

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<sup>1</sup>Applicant's unique citation designation number. <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.  
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